ABSTRACT

**METHOD AND APPARATUS FOR CONTEXT SWITCHING
IN COMPUTER OPERATING SYSTEMS**

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The present invention provides for a method and apparatus for a computer implemented system including a processor and cache memory arranged to receive operating system instructions for context switching between processes, and including control means for writing back cache data to memory means during processor idle cycle at completion of a process, and prior to initiation of the context switch so as to enhance the operating speed since, at the time of initiating the context switch, the cached data has already been written back to memory.